WEST Search History

DATE: Monday, May 12, 2003

Set Name	Query	Hit Count	Set Name
side by side			result set
	DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
L52	L51 and ((record$ or unrecord$) near5 (locations or areas))	13	L52
L51	L50 and (logical near5 volume)	13	L51
L50	L49 and (file same structure)	22	L50
L49	L48 and (address near5 allocat$)	23	L49
L48	(optical near5 disks) same (root near5 director$)	89	L48
L47	L46 and (reserve near5 address)	1	L47
L46	L45 and (address same volume)	136	L46
L45	l26 and ((root or parent) near5 (director$))	827	L45
L44	L42 and address	2	L44
L43	L42 and (director$ near5 address)	0	L43
L42	(root and director$).ti.	22	L42
L41	L39 and (invalid same extent)	4	L41
L40	L39 and extent	22	L40
L39	L38 and (logical near5 volume)	28	L39
L38	L16 and l26	99	L38
L37	L33 and (directory same structure)	2	L37
L36	L33 and (record$1 same structure)	0	L36
L35	L33 and (file same structure)	0	L35
L34	L33 and (file near5 structure)	0	L34
L33	L32 and (invalid near5 data)	16	L33
L32	(invalid near5 extent)	37	L32
L31	6101497.pn.	2	L31
L30	(invalid extent)	4	L30
L29	L28 and (invalid extent)	0	L29
L28	L26 and (storage near5 medium)	3514	L28
L27	L26 and (director$ near5 manaagement)	0	L27
L26	(file near5 management)	14369	L26
L25	L24 and (file near5 management)	2	L25
L24	L23 and (invalid near5 data)	16	L24
L23	(invalid near5 extent)	37	L23
L22	L19 and (invalid near5 extent)	0	L22
L21	L19 and (invalid near5 volume)	0	L21
L20	L19 and (invalid near5 data)	0	L20

L19	(optical and director$ and file$).ti.	26	L19
L18	L17 and root	2	L18
L17	L16 and (invalid near5 extent)	4	L17
L16	(optical near5 disk$1) and (invalid near5 data)	663	L16
L15	L11 and (invalid near5 data)	0	L15
L14	L13 and (invalid near5 data)	0	L14
L13	L11 and volume	36	L13
L12	L11 and ((volume) same (file structure))	0	L12
L11	(file$1 near5 director$).ti.	425	L11
L10	(4458315| 4462078| 4562495| 5513260| 5761301| 5778395| 5802174| 5883958| 5915018| 5917910| 5982886)![pn]	22	L10
L9	L8 and extent	2	L9
L8	(compact near5 disk$1) same (file near5 management)	12	L8
L7	L2 and (file near5 management)	0	L7
L6	L2 and (root near5 director$)	0	L6
L5	L2 and (voume near5 space)	0	L5
L4	L2 and (start address)	0	L4
L3	L2 and (start adj5 address)	0	L3
L2	(compact and disk$1 and structure).ti.	114	L2
L1	(compact and disk$1 and structure).ti,ab.	1427	L1

END OF SEARCH HISTORY

Google

root directory start address invalid data invalid extent

Google Search

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Searched the web for **root directory start address invalid data invalid extent**. Results 1 - 10 of about 13,100. S

Untitled
... image file system, indicates, in the obvious fashion, the **root directory** in the ... read
R4 = offset of first item to read in **directory** (0 for **start** of **directory** ...
www.pinknoise.demon.co.uk/Docs/ Arc/MultiFS/Register.html - 15k - Cached - Similar pages

Making GeodSoft.com: NT Problems, MMC Loses IIS Configuration
... to be sure change all the IP **address** in both ... ini file in the Windows NT **root directory**
that looks ... same configuration information displayed as the **start** menu or ...
geodsoft.com/about/making/ntdsl.htm - 28k - Cached - Similar pages

[PDF]Clustering LDAP **Directory** Servers
File Format: PDF/Adobe Acrobat - View as HTML
... 2. Change the default server **root directory**: /usr/netscape ... 3. Change the base install
directory pathname should be the location of where the **start** and **start** ...
www.sun.com/blueprints/1299/clustering.pdf - Similar pages

[PDF]Sun StorEdge N8400 Filer Release Notes Addendum
File Format: PDF/Adobe Acrobat - View as HTML
... syslog.conf File Back to the Disk Tray 1. **Start** an ftp ... Password: password 230 User
root logged in. ... Addendum 11 3. Access the /etc disk tray **directory** using the ...
www.sun.com/products-n-solutions/hardware/ docs/pdf/816-1595-10.pdf
- Similar pages
[More results from www.sun.com]

[PDF]Sun Cluster 2.2 7/00 **Data** Services Update: Tivoli 3.6
File Format: PDF/Adobe Acrobat - View as HTML
... use the GUI, do NOT select the "**start** at boot ... Then copy the **directory** to all other
possible masters of ... to create an administrator with user ID **root** and group ...
docs-pdf.sun.com/806-1938/806-1938.pdf - Similar pages

[PDF]Sun Cluster 2.2 7/00 **Data** Services Update: Lotus Domino 4.6.3
File Format: PDF/Adobe Acrobat - View as HTML
... **Start** the first node. ... Run the install command as **root**. ... The Lotus Domino installation
directory on the Lotus CD-ROM might vary from the **directory** shown here. ...
docs-pdf.sun.com/806-2687/806-2687.pdf - Similar pages
[More results from docs-pdf.sun.com]

OpenSpirit - Support - End-User FAQ - OpenSpirit Version 2.3.x - ...
... services by running "$OSP_HOME/bin/runOSServices.sh **start**". ... this will still refer
to the **root** userid ... your projects (using a different output **directory**, of course ...
www.openspirit.com/enduserfaq23xinstallation.html - 38k - Cached - Similar pages

Programming Assignment 1
... For instance, **root directory** could be a **directory** named pub ... as accessing non-existing file/**directory**, trying to ... server protocol FIRST - before you **start** coding ...
www.ecsl.cs.sunysb.edu/cse533/hw1.htm - 15k - Cached - Similar pages

[PDF]File & Buffer Management Storage Hierarchy Levels of Abstraction ...
File Format: PDF/Adobe Acrobat - View as HTML
... when concurrent threads require free **data** pages in ... buffer manager interface readdirect **directory** bufferfix (P ... frame **address** : Denote the **start address** of the ...
www.diku.dk/undervisning/2003f/ 729/slides/729-buffer.pdf - Similar pages

Manual for the raFS filing system
... 76.76.76.0) - word contains -1 for **root** 4 &80000000 ... 4*n offset to **start** of **directory** entries (words must ... in ASCII order, offsets are relative to **start** of these ...
home.in.tum.de/~alterer/riscos/rafs/tech.html - 41k - May 11, 2003 - Cached - Similar pages

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Result Page: 1 2 3 4 5 6 7 8 9 10   **Next**

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root directory data storage start address

Google Search

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Understanding FAT
... system logic level Sector: Single unit of **storage** at the ... FAT16 volumes, but FAT32
treats the **root directory** as just another cluster chain in the **data** area ...
users.iafrica.com/c/cq/cquirke/fat.htm - 11k - Cached - Similar pages

Web Hosting
... plus: 1. 10 GB Traffic 2. 150 MB of **Data Storage** 3. Unlimited ... Script application,
the mivadata **directory** is used as the **root directory** for the **data** files. ...
www.thisishowto.com/web_hosting.htm - 40k - May 11, 2003 - Cached - Similar pages

Your own "Personal Web Server"
... Java scripting and cookie **storage** required for proper display of ... Copy your WWW-**data**
to the shared "NET_HTML ... your Web-pages to your Web-**root directory** (in this ...

www.wown.com/j_helmig/pws.htm - 25k - Cached - Similar pages

Xindice internals
... the collection's **directory** somewhere in the database **root directory**. ... tree **data** structure
(which allows **storage** of (key ... 64-byte header, followed by actual **data**. ...
xml.apache.org/xindice/dev/guide-internals.html - 36k - Cached - Similar pages

Your own "Personal Web Server"
... Copy your WWW-**data** to the shared "NET_HTML" **directory** ... change the location of your
Web-**Root directory** or the ... us, Java scripting and cookie **storage** required for ...
www.wown.info/j_helmig/pws.htm - 22k - May 11, 2003 - Cached - Similar pages

Magnetic **Data Storage**
... of the boot sector, the FAT, and the **root directory**. ... determines the minimum allocation
unit of **data storage** on the ... the time it takes to transfer **data** from the ...
www.spurdens.freeserve.co.uk/pc/harddisk.htm - 30k - Cached - Similar pages

Computer Science and Networking Glossary-R
... represents the **root directory**, and you can use this character to make the **root directory** the current **directory** in a ... RXD Abbreviation for receive **data**. ...
www.angelfire.com/ny3/diGi8tech/RGlossary.html - 22k - Cached - Similar pages

[PDF]Microsoft PowerPoint - 1b11-4

File Format: PDF/Adobe Acrobat - View as HTML

... and disks interchangeably I Important **data** structures: o ... I Free space list and **root**

directory in fixed ... Implementation - 3 DOS-like I **Directory** contains **address** ...

www.cs.ucl.ac.uk/staff/s.wilbur/1b11/1b11-4.pdf - Similar pages

CCA-2-OSN Lecture - Week 4

... path name => absolute (ie, starting from **root**) => relative (with ... io classes work exclusively with **data** fork => format ... Q+N-1 to file => **directory** entry records ...

www.scism.sbu.ac.uk/ccsv/josephmb/ CS-L2-OS/osn/week4.html - 9k - Cached - Similar pages

Chapter 2 - Active **Directory Data Storage**

... through an attribute on the **root** object (rootDSE ... information about disk management, see **"Data Storage** and Management ... as a tombstone in the **directory** before being ...

www.microsoft.com/technet/prodtechnol/windows2000serv/ reskit/distsys/part1/dsgch02.asp - 101k - Cached - Similar pages

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root directory data storage start address | Google Search | Search within results

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Searching for PHRASE invalid extent reserve address space
Restrict to: Header Title Order by: Citations Hubs Usage Date Try: Amazon B&N Google (RI) Google
(Web) CSB DBLP
No documents match Boolean query. Trying non-Boolean relevance query.
1000 documents found. **Only retrieving 250 documents (System busy - maximum reduced).** Retrieving
documents... **Order: relevance to query.**

Virtual Shared Memory: A Survey of Techniques and Systems - Raina (1992) (Correct) (7 citations)
the cache-coherence protocol(writ-update/write-**invalid**ate, snoopy/directorybased etc.and
Such schemes are evidently sensitive to the **extent** of sharing in programs and broadcast is the usual
invalid, valid -not modified, possibly shared, **reserved** -not needing a write-back, guaranteed the only
www4.informatik.uni-erlangen.de/~tsthiel/Papers/ddm-vsm.ps.gz

Coordinated Checkpointing-Rollback Error Recovery for.. - Janakiraman, Tamir (1994) (Correct) (30 citations)
ownership of a block prior to modifying it, by **invalid**ating all other copies. Each block of the
the presence of a shared and partially replicated **address space**, and the presence of a distributed
of a shared and partially replicated **address space**, and the presence of a distributed coherency
ftp.cs.ucla.edu/tech-report/94-reports/940027.ps.Z

Implementation and Performance of the Mungi.. - Heiser.. (1997) (Correct) (10 citations)
are revalidated periodically to detect **invalid**ations by the owners of the Clists (c.f. Sect.
Implementation and Performance of the Mungi Single-**Address-Space** Operating System Gernot Heiser Kevin
and Performance of the Mungi Single-**Address-Space** Operating System Gernot Heiser Kevin Elphinstone
ftp.cse.unsw.edu.au/pub/doc/papers/UNSW/9704.ps.Z

Multicomputer object stores: the Multicomputer Texas experiment - Stephen Blackburn (1996) (Correct)
(3 citations)
purposes of this paper, scalability refers to the **extent** to which larger problems within a particular
of mappings from persistent identifiers to process **address**es under a distributed memory implementation of a
include creating a single image of an object **space** across multicomputer architectures, tradeoffs
cs.anu.edu.au/~Steve.Blackburn/pubs/pos7.ps.gz

Using Write Protected Data Structures To Improve Software Fault.. - Sullivan (1991) (Correct) (6 citations)
marks the current version of the record as **invalid** and inserts a new version of the record on the
may have to go through recovery simply because the **extent** of propagated damage is not known. Unfortunately,
and testing them increases development cost. To **address** software error propagation, we have modified a
s2k-ftp.cs.berkeley.edu:8000/postgres/papers/ERL-M91-56.ps.Z

Packets Found on an Internet - Bellovin (1992) (Correct) (4 citations)
ICMP implementations will ignore messages with **invalid** code fields, recent RFC's
we are contemplating adding that ability. 2 **Address Space** Oddities Our setup for monitoring **address**
are contemplating adding that ability. 2 **Address Space** Oddities Our setup for monitoring **address space**
www.raptor.com/lib/./packets.ps

Compile/Run-time Support for Threaded MPI Execution on.. - Tang, Shen, Yang (1999) (Correct)
may still hold a reference and can result in **invalid** memory references. Herlihy [18] proposed a
example, SGI provides a service called "miser" to **reserve** a dedicated processor pool and a non-dedicated
1 An Earlier Version Of Sgi Mpi Enforced That The **Address Space** Of Each Mpi Process Is Shared With Every
www.cs.ucsb.edu/TRs/techreports/TRCS98-30.ps

Moving The Shared Memory Closer To The Processors - DDM - Hagersten, Landin, Haridi (1991) (Correct)
each cache line can be in one of the four states **INVALID**, VALID, **RESERVED**, or DIRTY. Many caches might
however, burden the software to a much greater **extent** in that it has to statically distribute the
can be in one of the four states **INVALID**, VALID, **RESERVED**, or DIRTY. Many caches might have the same
ftp.sics.se/pub/SICS-reports/Reports/SICS-R--90-17--SE.ps.Z

Network Related Performance Issues and Techniques for MPPs - Johnsson (1995) (Correct)
rows for a grid where the axes of stride one has **extent** M .Similarly, a seven-point stencil in three

area of research. We will discuss configuring the **address space** in the context of a few applications in
research. We will discuss configuring the **address space** in the context of a few applications in the next
ftp.das.harvard.edu/techreports/tr-29-95.ps.gz

Beyond Multiprocessing ... Multithreading the SunOS.. - Eykholt, Kleiman.. (1992)   (Correct)   (72 citations)
SunOS 5.0 implements those interfaces to the **extent** defined so far, using our locking primitives and
does not require a change of virtual memory **address space** information, so it is relatively
does not require a change of virtual memory **address space** information, so it is relatively inexpensive.
sunsite.unc.edu/pub/sun-info/development-tools/multi-threaded/beyond_mp.ps

The Beehive Cluster System - Aman Singla   (Correct)
shared memory read sh_write shared memory write (**invalid**ation based) pset_write explicit shared memory
design features of Beehive include: a global **address space** across the cluster (Section 5)a
features of Beehive include: a global **address space** across the cluster (Section 5)a configurable
www.cc.gatech.edu/computing/Architecture/Beehive/../papers/beehive.ps.gz

Shasta: A Low Overhead, Software-Only Approach for Supporting.. - Daniel Scales (1996)   (Correct)   (26 citations)

in the Shasta system has three basic states: ffl **invalid** -the data is not valid on this processor. ffl
describes Shasta, a system that supports a shared **address space** in software on clusters of computers with
Shasta, a system that supports a shared **address space** in software on clusters of computers with
www.research.digital.com/wrl/people/scales/papers/asplos96.ps

On Using Intelligent Network Interface Cards to.. - Fiuczynski, Martin, .. (1998)   (Correct)   (2 citations)
.Performance. SPINE extensions run in the same **address space** as the firmware, with low-overhead access
SPINE extensions run in the same **address space** as the firmware, with low-overhead access to
interfaces provide support for multiple **address spaces**. A safe language enables extensions and SPINE to
www.cs.berkeley.edu/~rmartin/papers/mef-nossdav98.ps

Application-Controlled Physical Memory using External Page-Cache.. - Harty (1992)   (Correct)   (81 citations)
discarding of pages within its physical memory. **Address**ing these problems has significant performance
an application may be able to make an intelligent **space**-time tradeoff between different algorithms or
With control of the portion of its virtual **address space** mapped to physical memory, an application can
www.cs.berkeley.edu/~brewer/cs262/hc.ps

Towards Object-based Wide Area Distributed Systems - v.Steen, Homburg, van.. (1995)   (Correct)   (7 citations)
interfaces and state distributed across multiple **address spaces**. Such a distribution is realized through
and state distributed across multiple **address spaces**. Such a distribution is realized through local
state, methods, and interfaces in a single **address space**. A distributed object is a collection of local
www.cs.vu.nl/~philip/papers/iwooos95.ps.Z

Evaluation of All-Software Conventional Distributed Shared.. - Arif Bhatti (1997)   (Correct)
[10, 11]2.3 Coherence Protocol An **invalid**ation protocol allows multiple copies of a data
this abstraction in hardware by providing global **address space** and coherent caches. To implement cache
abstraction in hardware by providing global **address space** and coherent caches. To implement cache coherence
www.cs.bu.edu/students/grads/tahir/ccc97.ps

Table 51: Topology error tests - Test Path   (Correct)
fortran/topo/error/ MPI_Graph_neighbors_count_err2 **Invalid** communicator (MPI_COMM_NULL)
ftp.irisa.fr/pub/mirrors/netlib/mpi/mpi-1.1-test/mpitest_guide.ps.Z

Matching Micro-Kernels to Modern Applications using.. - Ciaran Bryce (1995)   (Correct)   (3 citations)
calls to supervisor DPs, ii) selectively **invalid**ating TLB entries. 3.3.1 Optimizing PPC for
our approach relies on the assignment of a single **address space** to an application with application modules
relies on the assignment of a single **address space** to an application with application modules having
www.fcul.research.ec.org/broadcast/trs/../papers/97.ps

A Hierarchical Mobility Management Scheme for IPv6 - Claude Castelluccia   (Correct)
mobility. In fact, the Internet's routing and **address** structure prohibits packets **address**ed to a
We show that while our approach has a larger memory **space** requirement, it reduces the network load,
it greatly takes advantage of the larger **address space** and new features of IPv6, but it could also be

ballesta.inrialpes.fr/infos/Personnes/Claude.Castelluccia/iscc98.ps.gz

<u>Kernel-Level Threads for Dynamic, Hard Real-Time Environments - Marty Humphrey (1995)</u>   (Correct)
<u>(2 citations)</u>
Page 21 Map Page 50 Flush Entry 10 Load Entry 10 **Invalid**ate Page 17 Flush Map 8 Load Map 420 Restore
process model, a thread model separates the **address space** from the flow of execution within the
process model, a thread model separates the **address space** from the flow of execution within the **address** .
counter.cs.umd.edu/~rich/courses/cmsc818G-s98/papers/spring_kernel.ps

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No documents match Boolean query. Trying non-Boolean relevance query.

1000 documents found. **Only retrieving 125 documents (System busy - maximum reduced).** Retrieving documents... **Order: relevance to query.**

MacFS: A Portable Macintosh File System Library - Dinda, Necula, Price (1998) (Correct)
to them as such. Each **directory**, including the **root directory**, contains its **directory** thread, which contain four kinds of items: files, directories, **directory** threads, and file threads. Each item is blocks. An **extent** descriptor encodes an **extent's start**ing block and length into a 32 bit quantity. The reports-archive.adm.cs.cmu.edu/anon/1998/CMU-CS-98-145.ps

Table 51: Topology error tests - Test Path (Correct)
Calling MPI_Sendrecv_replace(to send message from **root** to all other ranks in communicators. Test loops ftp.irisa.fr/pub/mirrors/netlib/mpi/mpi-1.1-test/mpitest_guide.ps.Z

Integrating Temporal, Real-Time, and Active Databases - Ramamritham.. (1996) (Correct) (3 citations)
this paper we outlined the approaches developed to **address** these problems. Since most situations offer actions if temporal **data** is (about to become) **invalid** and to shed loads as well as adjust deadlines Integrating Temporal, Real-Time, and Active **Data**bases Krithi Ramamritham, Raju Sivasankaran, John www-ccs.cs.umass.edu/~sim/sigrec96.ps

Software Assistance for Directory-Based Caches - Li (1994) (Correct) (1 citation)
Software Assistance for **Directory**-Based Caches Zhiyuan Li Department of Computer of var 2 mod(m) or var 2 use(m)g)3. DE(B) de(**start**)where **start** is the **start** basic block of B. special, but simple, treatment [13]We do not **address** this trivial matter here. 3.2 The marking www-users.cs.umn.edu/Research/Agassiz/Paper/li.ipps94.ps.Z

Scheduling Access To Temporal Data In Real-Time Databases - Xiong, Sivasankaran.. (1997) (Correct) (3 citations)
valid in the interval [t 0 t 4]Transaction T **starts** at time t 1 ,and T has no **data**-deadline at this The model considered is general enough to **address** one of the most important issues in real-time any of the temporal **data** objects they read become **invalid** before the transaction commits. Scheduling www-ccs.cs.umass.edu/~sim/rtdb-chapter96.ps

A Proposal for a User-Level, Message-Passing Interface in a.. - Jack Dongarra (1993) (Correct) (23 citations)
any additional structure. As one consequence, new **root** groups can only be defined by listing the of the location in buf at which each **data** block **starts** nblk number of **data** blocks to be scattered that not only maps a PID number to an Internet **address** and process ID on the destination machine, but softlib.rice.edu/pub/CRPC-TRs/reports/CRPC-TR93437.ps.gz

A dynamic IP addressing system for Internet telephony.. - Siu-Cheung Hui (Correct)
in a hierarchical tree with the world as the **root** of the tree. Each sub-tree pertains to individual or off-line methods, namely, Electronic Mail and **Directory** Service Lookup, are proposed, implemented and operation is illustrated in Fig. 14. 23 On-line **Start** Retrieve IP **address** from recipient's URL booch.sas.ntu.ac.sg:8080/cc98.ps

Partial Scan Design Based on Circuit State Information - Dong Xiang (1996) (Correct) (4 citations)
sequence or the reset state is given, we can **start** logic simulation from the reset state. Otherwise, effective test generator may traverse only a few **invalid** states that cause backtracks. Therefore, the composer.ecn.purdue.edu/~fuchs/fuchs/dacSV96.ps

The UNIX Time-Sharing System - Ritchie, Thompson (1974) (Correct) (137 citations)
directories for its own use. One of these is the **root directory**. All files in the system can be found by on the file system as a whole. Each user has a **directory** of his own files he may also create they will appear in core memory when the program **starts** executing. A few user programs manipulate files cm.bell-labs.com/cm/cs/who/dmr/cacm.ps

● ●

Interface Timing Verification with Application to Synthesis - Elizabeth Walkup (1994)   (Correct)   (7 citations)
algorithm is T ,where T is the number of distinct **root**ed trees over the constraint set C. This bounds the
Number of Pass Through Outer Repeat Loop Node **start** 1 st Updates 1 st SC 2 nd Updates AV 0 0 0
To Interface With The Trctrc Tclz Taa Tacs Toh **Address** Cs **Data_**out Fig. 1: Timing Diagram For An Sram
www.cs.washington.edu/research/lis/papers/postscript/walkup-dac94.ps

Interface Timing Verification with Combined Max and Linear.. - Elizabeth Walkup (1994)   (Correct)   (1 citation)
algorithm is T ,where T is the number of distinct **root**ed trees which can be induced by the constraint set
Number of Pass Through Outer Repeat Loop Node **start** 1 st Updates 1 st SC 2 nd Updates AV 0 0 0
Two Types Of Temporal Trctrc Tclz Taa Tacs Toh **Address** Cs **Data_**out Figure 1: Timing Diagram For An Sram
ftp.cs.washington.edu/tr/1994/03/UW-CSE-94-03-04.PS.Z

A Model for Worldwide Tracking of Distributed Objects - van Steen, Hauck, Tanenbaum (1996)   (Correct)
(5 citations)
per object, a path of forwarding pointers from the **root** to each leaf node where a contact **address** is
client processes. With each region we associate a **directory** node, capable of storing contact points that
address is found through a search path up the tree, **start**ing at the leaf node identified by the region
www.cs.vu.nl/pub/papers/globe/tina.96.ps.Z

The High Performance File System - Kurotsuchi (1996)   (Correct)
the **directory** band (discussed later)and of the **root directory**. The date of the last time the
free space bitmap, the bad block list, and the **directory** band (discussed later)and of the **root**
of two 32-bit words, the first containing the **start** sector of the **extent** the second is the **extent**
www.csc.calpoly.edu/~bkurotsu/hpfs.ps.gz

Improving data cache performance by pre-executing instructions .. - Dundas, Mudge (1997)   (Correct)   (4 citations)

can be generated. After the cache miss that **start**ed runahead mode is serviced the processor resumes
an L1 **data** cache miss it records the instruction **address** of the faulting load or store instruction and
for pre-executed instructions to be dependent upon **invalid data**. Rather than terminating runahead we allow
www.eecs.umich.edu/~tnm/papers/ics.ps

Bottom-up Induction of Functional Dependencies from Relations - Savnik, Flach (1993)   (Correct)   (10 citations)
properties: 1. Every node of the tree, except the **root** node, is an attribute. 2. The children of the node
differs from the top-down approach in that it **start**s with an analysis of the tuples in the relation: a
the relation is derived from the fact, that all **invalid** dependencies that are contradicted by a given
ftp-csd.ijs.si/Reports/CSD-TR-93-3.ps.gz

Installation and using of ACE under SRT/CVS for the ATLAS.. - Authors Rybaltchenko   (Correct)
been made: 1. The contents of the ACE_ROOT/ace **directory** was split into three different parts: i. All
- PMG Agent and Client. Client invokes the **Start_**Proc method of PMG_T_Server_impl on PMG Agent
and server allowing them to connect and exchange **data**. No command line arguments are needed to run the
atddoc.cern.ch/Atlas/Notes/../postscript/Note087.ps

Design and Evaluation of a Subblock Cache Coherence Protocol.. - Anderson, Baer (1994)   (Correct)   (3 citations)

and bus traffic. Other authors have proposed **directory** based schemes in which the unit of coherence is
for only the final 5 iterations to eliminate cold-**start** effects. This represents the simulation of 62
Anderson and Jean-Loup Baer May 11, 1994 Email **address**es: craig@cs.washington.edu,
casaturn.kaist.ac.kr/~sikang/course/CS614/AB94.ps.gz

Using Subpages for Coherency Control in Parallel Database Systems - Andreas Listl (1994)   (Correct)
the cache coherence problem [4] or the buffer **invalid**ation problem [6]A number of proposals have
Using Subpages for Coherency Control in Parallel **Data**base Systems Andreas Listl Department of
www3.informatik.tu-muenchen.de/public/projekte/sfb342/publications/94.Listl.LNCS817.ps.gz

PMoct: A Policy management tool for OCT based Design Systems.. - Granacki, Kazi (1993)   (Correct)
Specifying and maintaining the semantics of **data** used by CAD/CAE tools is often accomplished
ftp.isi.edu/isi-pubs/rr-93-387.ps.Z

Efficient Convertible Undeniable Signature Schemes (Extended .. - Michels, Stadler (1997)   (Correct)   (3 citations)
to convince a verifier about the validity or **invalid**ity of a signature. Non-repudiation is still
pp. 470-484. 7] D.Chaum, T.Pedersen, Wallet **data**bases with observers"LNCS 740, Proc. Crypto'92,
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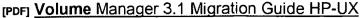File Format: PDF/Adobe Acrobat - View as HTML
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www.cs.columbia.edu/~nieh/teaching/e6118_s00/papers/sweeney_xfs96.pdf

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a specific grid. We yet do not know to what **extent file** access patterns will exhibit the locality
www.cs.uchicago.edu/~krangana/papers/p_grid_computing_camera.pdf

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subsystem or directly read it from an input **file. EXTENT** translates the formula to a program for the
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